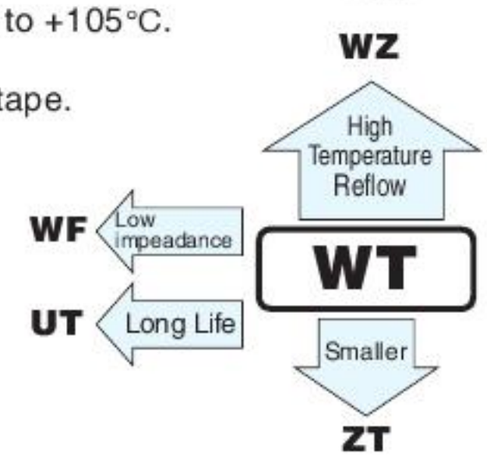
ALUMINUM ELECTROLYTIC CAPACITORS

nichicon

WT Chip Type, Wide Temperature Range series



- Chip type operating over wide temperature range of to –55 to +105°C.
- Designed for surface mounting on high density PC board.
- Applicable to automatic mounting machine fed with carrier tape.
- Compliant to the RoHS directive (2011/65/EU).



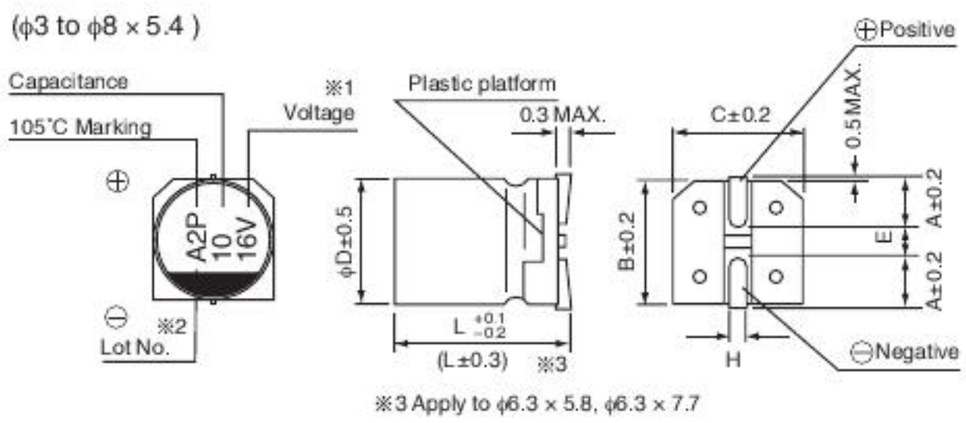


■Specifications

Item	Performance Characteristics
Category Temperature Range	–55 to +105°C
Rated Voltage Range	4 to 50V
Rated Capacitance Range	0.1 to 1500μF
Capacitance Tolerance	±20% at 120Hz, 20°C
Leakage Current	After 2 minutes' application of rated voltage, leakage current is not more than 0.01CV or 3 (μA) , whichever is greater.
Tangent of loss angle (tan δ)	Measurement frequency : 120Hz at 20°C Rated voltage (V): 4 / 6.3 / 10 / 16 / 25 / 35 / 50 tan δ (MAX.): 0.40 / 0.30 / 0.24 / 0.20 / 0.16 / 0.14 / 0.14
Stability at Low Temperature	Measurement frequency : 120Hz Rated voltage (V): 4 / 6.3 / 10 / 16 / 25 / 35 / 50 Impedance ratio ZT / Z20 (MAX.) Z–25°C / Z+20°C: 7 / 4 / 3 / 2 / 2 / 2 / 2 Impedance ratio ZT / Z20 (MAX.) Z–40°C / Z+20°C: 15 / 8 / 8 / 4 / 4 / 3 / 3
Endurance	The specifications listed at right shall be met when the capacitors are restored to 20°C after the rated voltage is applied for 1000 hours at 105°C. Capacitance change: Within ±25% of the initial capacitance value for capacitors of φ3mm unit, and 16V or less. Within ±20% of the initial capacitance value for capacitors of 25V or more. tan δ: 200% or less than the initial specified value Leakage current: Less than or equal to the initial specified value
Shelf Life	After storing the capacitors under no load at 105°C for 1000 hours and then performing voltage treatment based on JIS C 5101-4 clause 4.1 at 20°C, they shall meet the specified values for the endurance characteristics listed above.
Resistance to soldering heat	The capacitors are kept on a hot plate for 30 seconds, which is maintained at 250°C. The capacitors shall meet the characteristic requirements listed at right when they are removed from the plate and restored to 20°C. Capacitance change: Within ±10% of the initial capacitance value tan δ: Less than or equal to the initial specified value Leakage current: Less than or equal to the initial specified value
Marking	Black print on the case top.

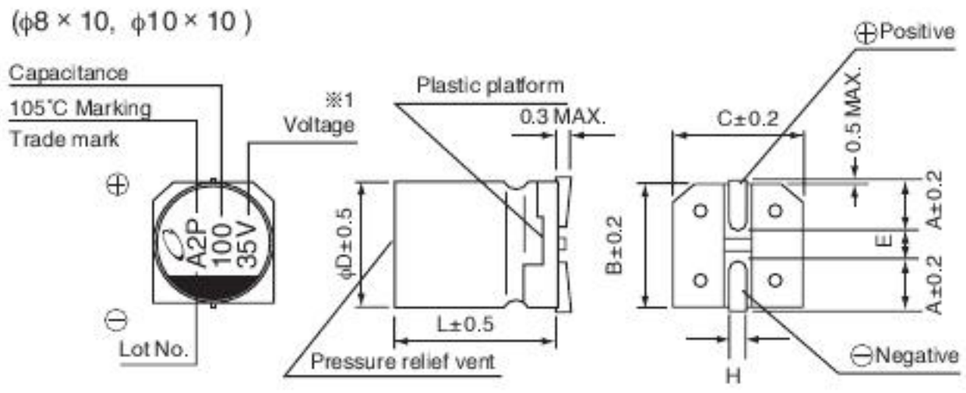
■Chip Type

(φ3 to φ8 × 5.4)



※3 Apply to φ6.3 × 5.8, φ6.3 × 7.7

(φ8 × 10, φ10 × 10)



※1. Voltage mark for 6.3V is 「6V」. In case of marking for φ3 units, "V" for rated voltage is omitted.
※2. In case of marking for φ3 units. Lot No is expressed by a digit (month code).

Type numbering system (Example : 16V 10μF)

1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	W	T	1	C	1	0	0	M	C	L	1	G	B

φD	code
3 to 8 (5.4L)	GB
6.3 to 10 (L≧ 5.8)	GS

Package code

Size code

φD	code
3	2
4 to 10	1

Configuration

φD	code
3 to 8 (L≦7.7)	CL
8 to 10 (L = 10)	NL

Capacitance tolerance (±20%)

Rated capacitance (10μF)

Rated voltage (16V)

Series name

Type

(mm)

φD × L	3 × 5.4	4 × 5.4	5 × 5.4	6.3 × 5.4	6.3 × 5.8	6.3 × 7.7	8 × 5.4	8 × 10	10 × 10
A	1.5	1.8	2.1	2.4	2.4	2.4	3.3	2.9	3.2
B	3.3	4.3	5.3	6.6	6.6	6.6	8.3	8.3	10.3
C	3.3	4.3	5.3	6.6	6.6	6.6	8.3	8.3	10.3
E	0.8	1.0	1.3	2.2	2.2	2.2	2.3	3.1	4.5
L	5.4	5.4	5.4	5.4	5.8	7.7	5.4	10	10
H	0.5 to 0.8	0.5 to 0.8	0.5 to 0.8	0.5 to 0.8	0.5 to 0.8	0.5 to 0.8	0.5 to 0.8	0.8 to 1.1	0.8 to 1.1

●Dimension table in next page.